



2186

AMENDMENT TRANSMITTAL LETTER				Docket No. M4065.0340/P340	
Application No. 09/652,003		Filing Date August 31, 2000		Examiner W. Choi	
				Art Unit 2186	
Applicant(s): Graham Kirsch					
Invention: METHOD AND APPARATUS FOR CONNECTING A MASSIVELY PARALLEL PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT SERIAL MANNER					
TO THE COMMISSIONER FOR PATENTS				RECEIVED APR 21 2003	
Transmitted herewith is an amendment in the above-identified application.				Technology Center 2100	
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	48	- 48 =		x	0.00
Independent Claims	7	- 7 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
<input checked="" type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ _____. A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge and credit Deposit Account No. <u>04-1073</u> as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
Thomas J. D'Amico Attorney Reg. No.: 28,371				Dated: April 17, 2003	
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 2101 L Street NW Washington, DC 20037-1526 (202) 828-2232 Customer No. 24998					

#11
4/24/03



Docket No.: M4065.0340/P340
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Graham Kirsch

Confirmation No.: 2935

Application No.: 09/652,003

Group Art Unit: 2186

Filed: August 31, 2000

Examiner: W. Choi

For: METHOD AND APPARATUS FOR
CONNECTING A MASSIVELY PARALLEL
PROCESSOR ARRAY TO A MEMORY
ARRAY IN A BIT SERIAL MANNER

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APR 21 2003

Technology Center 2100

AMENDMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In response to the Office Action dated January 17, 2003 (Paper No. 8), please
amend the above-identified U.S. patent application as follows:

IN THE CLAIMS

✓
Please rewrite claims 41 and 43 as follows:

B1 41. A method for writing data from a processing element to a memory device
comprising the steps of:

providing a plurality of data bits in a serial manner from said processing element to a data
circuit;

passing said data through said data circuit; and